

S/N 09/551027

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wendell P. Noble Jr. et al. Examiner: Michael Trinh
Serial No.: 09/551027 Group Art Unit: 2822
Filed: April 17, 2000 Docket No: 303.379US2
Title: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL WITH
VERTICAL TRANSISTOR AND TRENCH CAPACITOR

PETITION FOR A ONE-MONTH EXTENSION OF TIME

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Final Rejection mailed September 4, 2003, said period of response being extended from December 4, 2003 to January 4, 2004 (Sunday).

**Our check in the amount of \$110.00 is enclosed to cover the required extension fee.
Please charge any additional fees or credit overpayment to deposit Account No. 19-0743.**

Respectfully Submitted

WENDELL P. NOBLE JR. ET AL.

By their Representatives,

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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(612) 373-6969

Date: 1/5/04

By: 

Viet V. Tong

Reg. No: 45,416

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelop addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 5th day of January 2004.

Name

Signature